

FIG.1

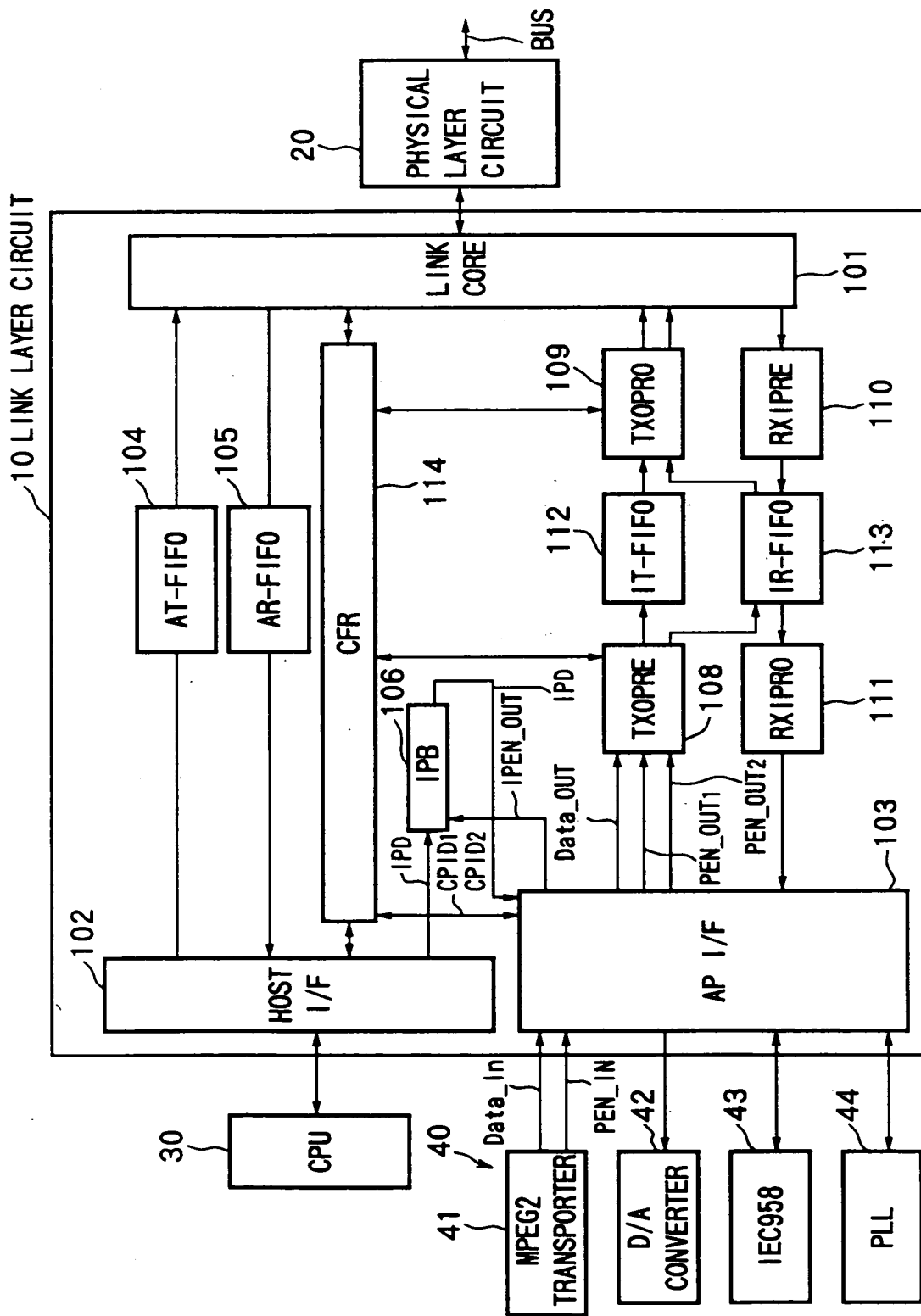


FIG.3

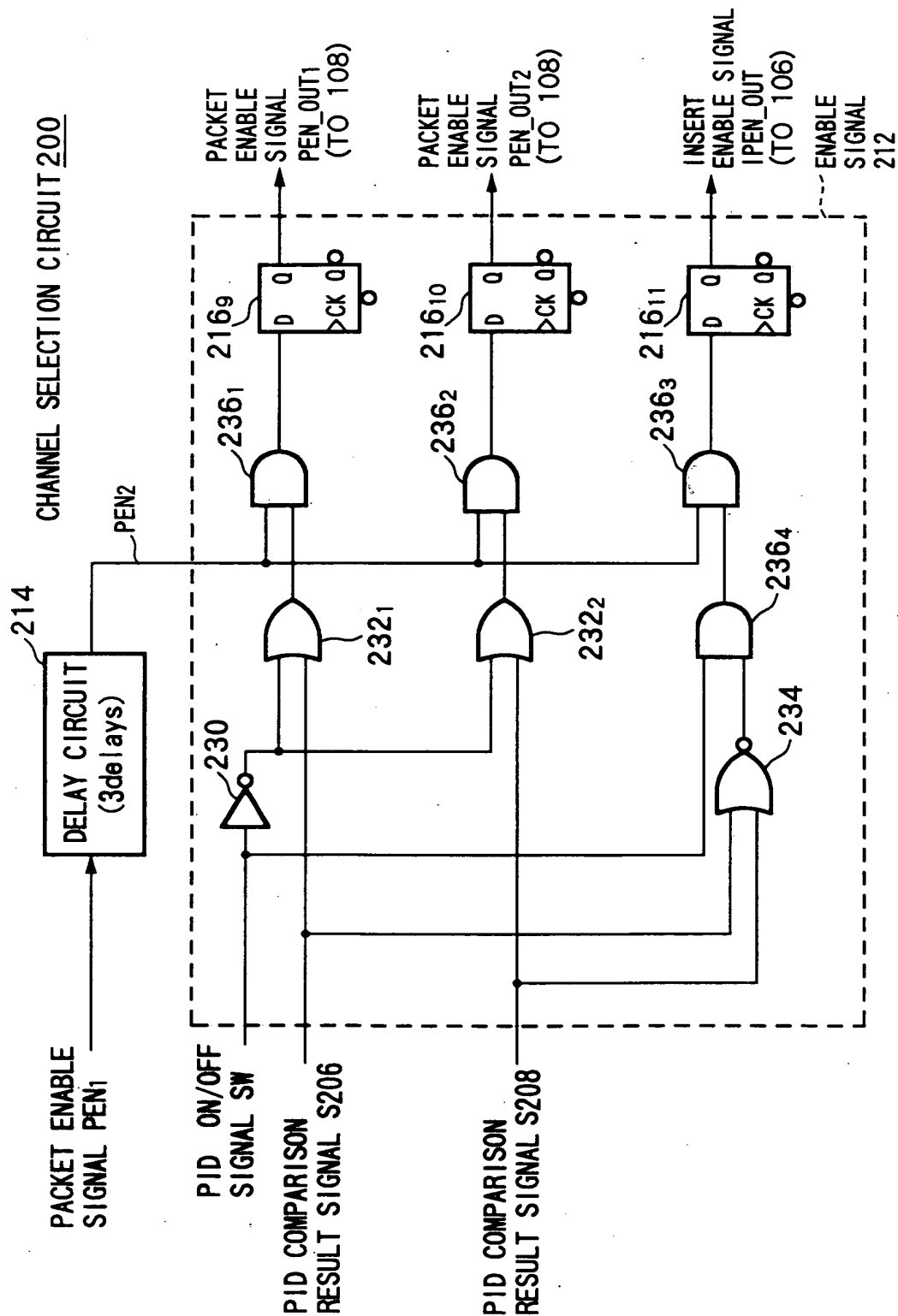


FIG.4A CK

CK

FIG.4B PEN_IN

PEN_IN

FIG.4C Data_IN

Data IN

FIG.4D **PEN₁**

PEN₁

FIG.4E Data1

Data1

FIG.4F **Data2**

Data2

FIG.4G **EDGE₁**

EDGE₁

FIG.4H **EDGE2**

EDGE2

FIG.4I **S204**

S204

FIG.4J **PID₁**
(Data3)

(Data3)

FIG.4K

PID2

FIG.4L S206

S206

FIG.4M PEN_OUT₁

PEN OUT₁

FIG.4N S208

S208

FIG.40 PEN_OUT₂

PEN_OUT2

FIG.4P DATA_OUT

DATA_OUT

FIG.4Q SW

SW

FIG.4R PEN_2

PEN₂

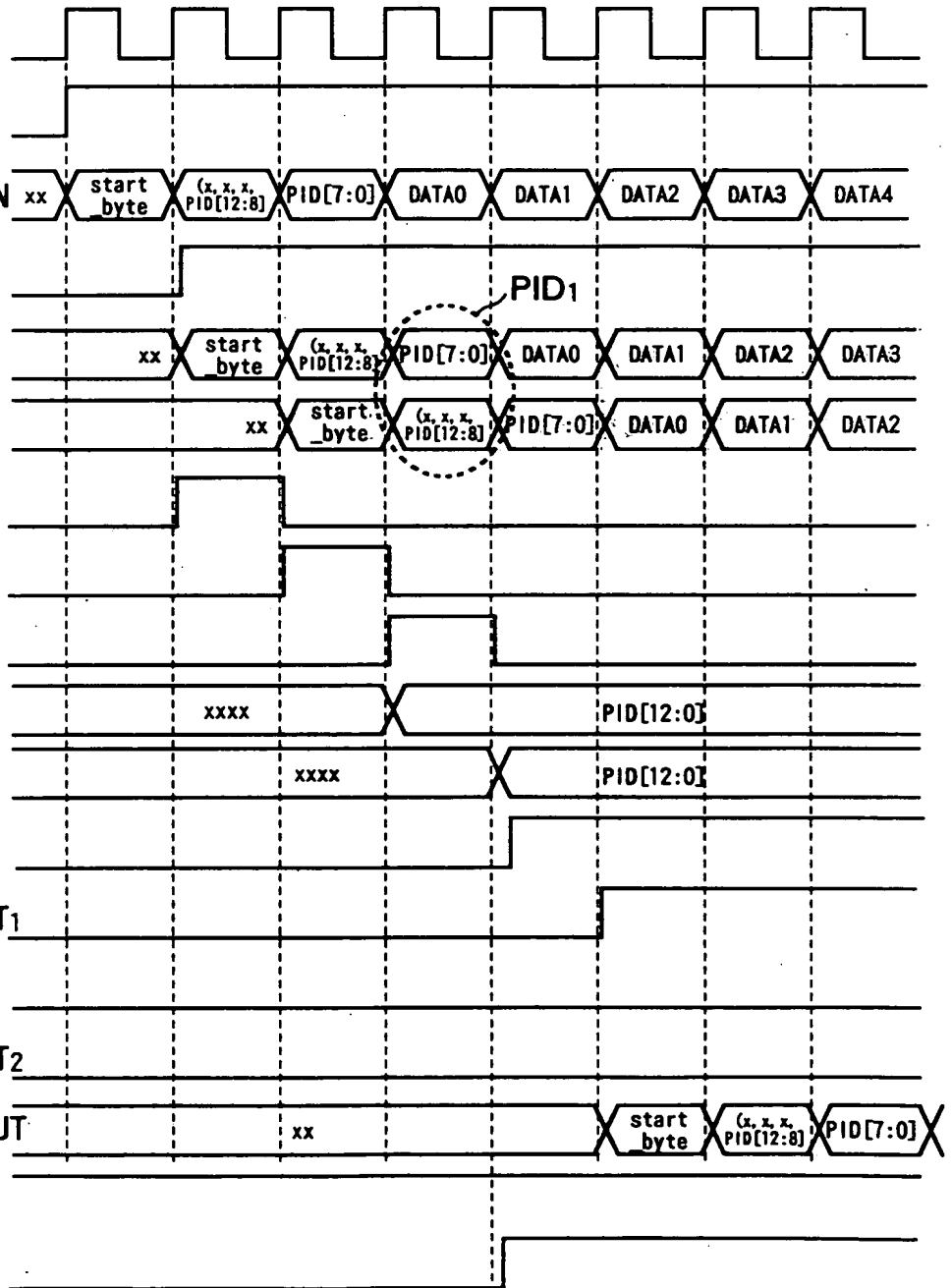


FIG.5

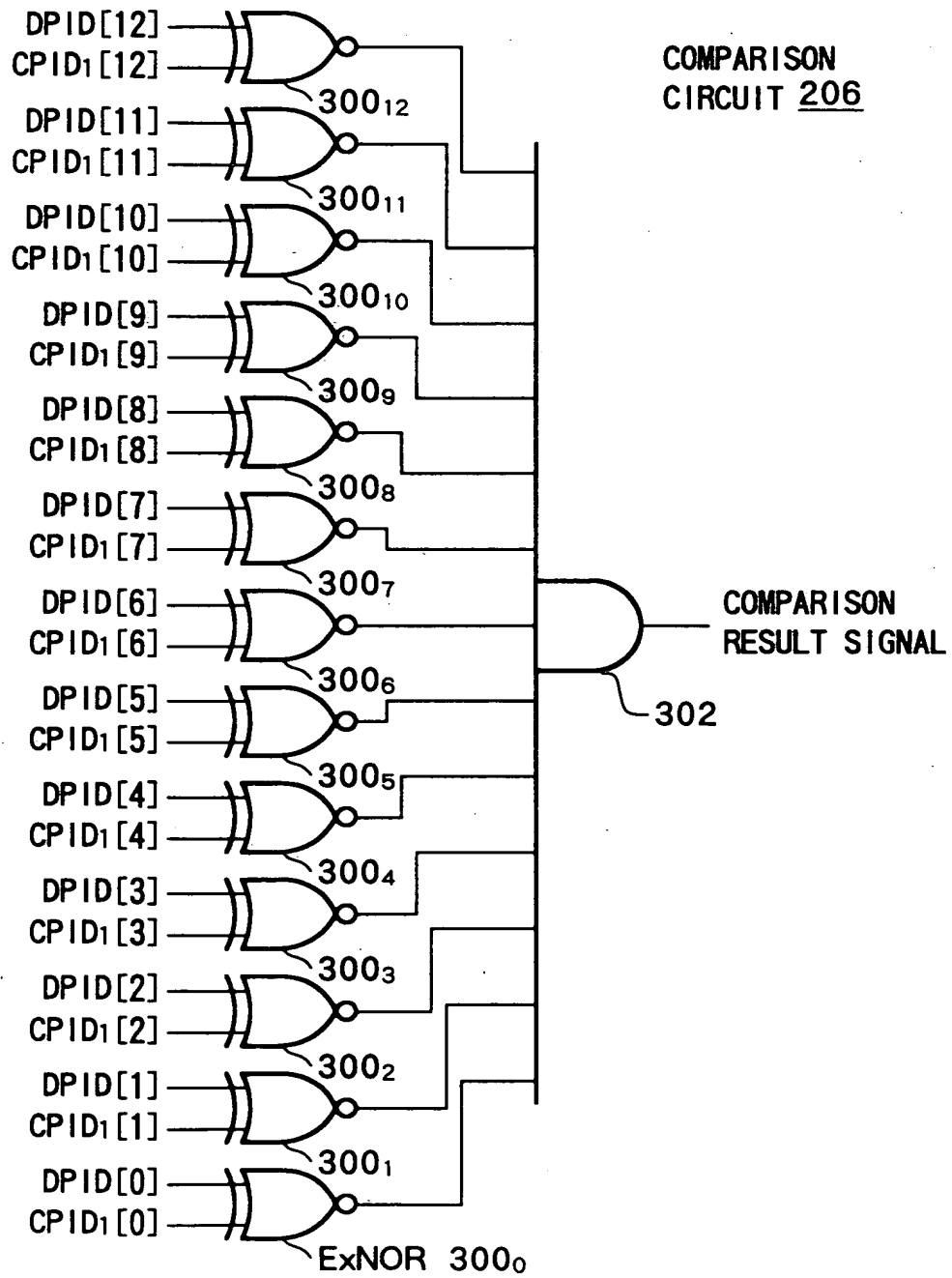


FIG.6

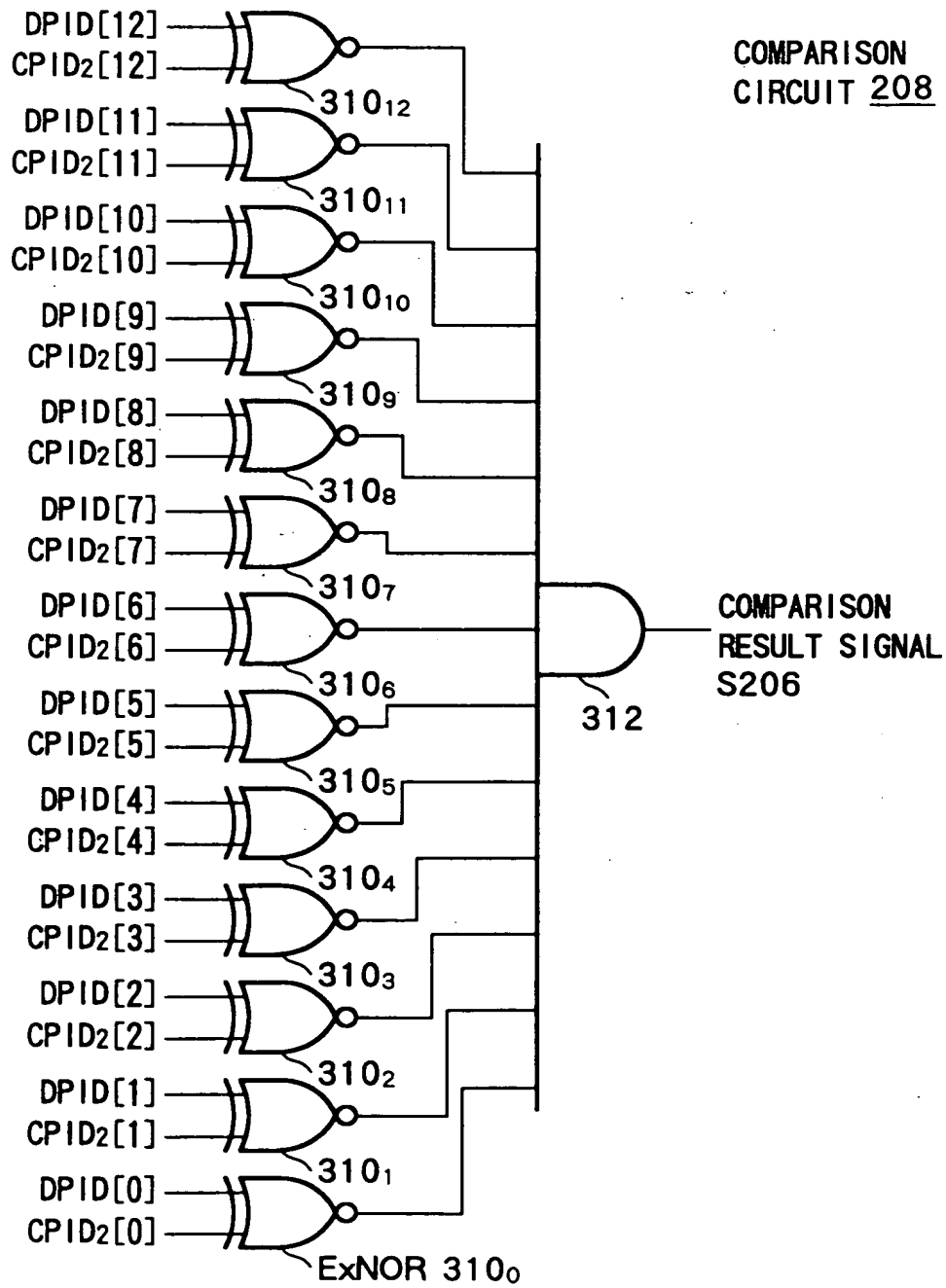


FIG. 7

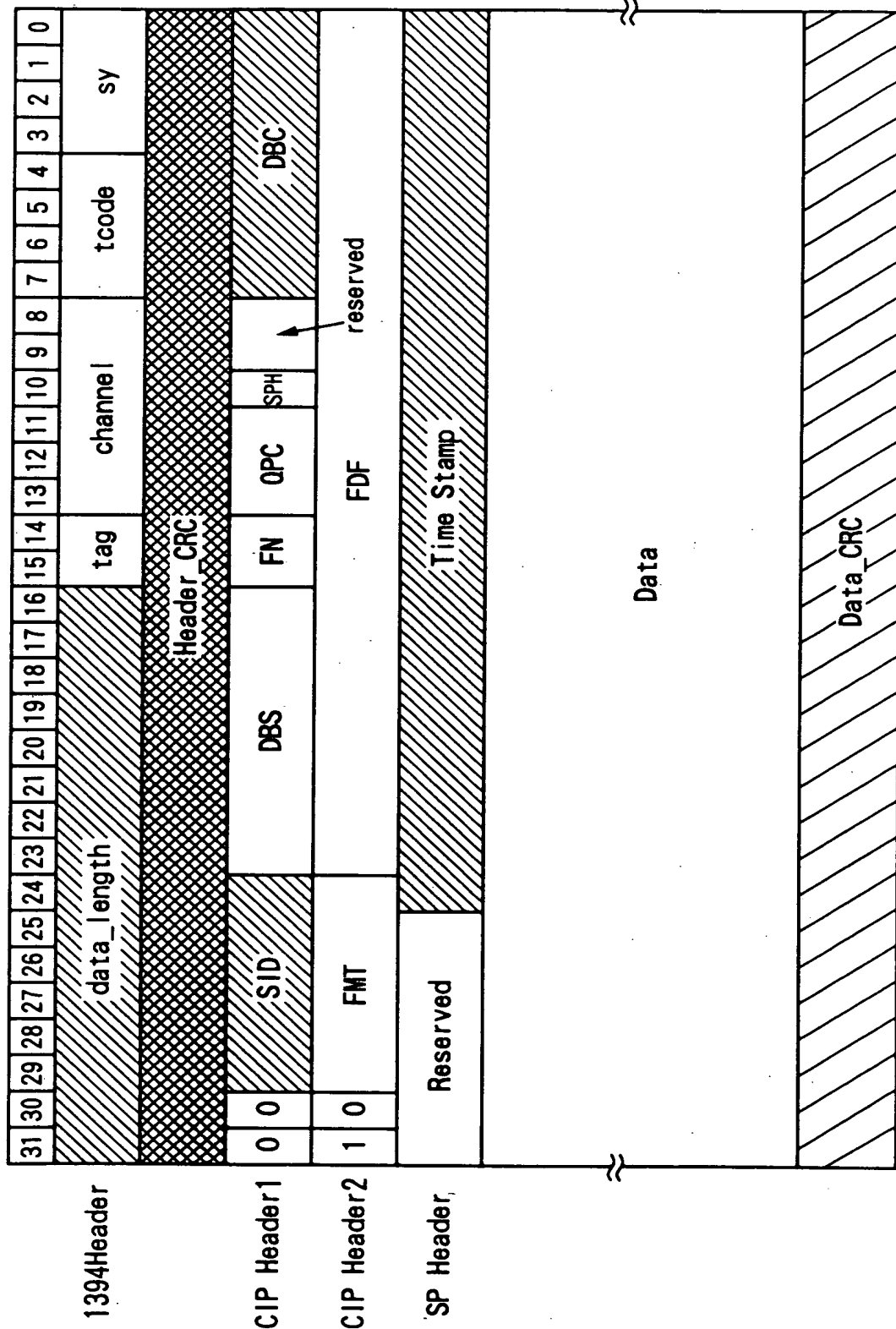


FIG.8

IEEE1394BUS

